

Applicant's Docket No. FORE-54

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of: Vishnu, Meenarachagan

Application No.: 09/503,673

Group No.: 2665

Filed: February 14, 2000

Examiner: Thien D. Tran

For: METHOD AND APPARATUS FOR DYNAMIC BITMAP GENERATOR SCHEDULER

Commissioner for Patents

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Technology Center 2600

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1. The information disclosure statement transmitted herewith is being filed *after* three months of the filing date of this national application or the date of entry of the national stage as set forth in Section 1.491 in an international application or after the mailing date of the first Office action on the merits, whichever event occurred last but *before* the mailing date of either

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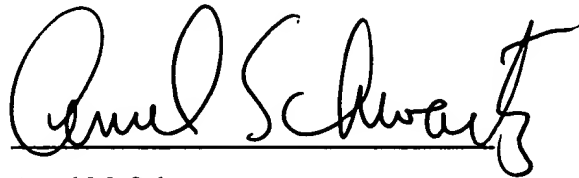
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Ansel M. Schwartz  
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of

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**Complete if Known**

Application Number

09/503.673

Filing Date

02/14/2000

First Named Inventor

Meenarachagan Vishnu

**Art Unit**

2665

Examiner Name

Thien D. Tran

Attorney Docket Number

FORE-54

## U. S. PATENT DOCUMENTS

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**FOREIGN PATENT DOCUMENTS**

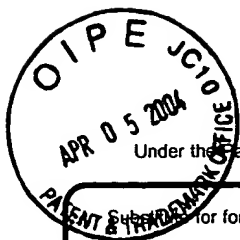
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Examiner Name

Thien D. Tran

Attorney Docket Number

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|--------------------|-----------------------|---|----------------|
|                    | ✓                     | HUI HUANG ET AL., "Hierarchical scheduling with adaptive weights for W-ATM," IEEE ATM WORKSHOP. PROCEEDINGS, XX, XX, p. 337-342,  |                |
|                    | ✓                     | CHIUSSI F.M. ET AL., "Implementing fair queueing in ATM switches: the discrete-rate approach," INFOCOM '98. SEVENTEENTH ANNUAL JOINT CONFERENCE OF THE IEEE COMPUTER AND COMMUNICATIONS SOCIETIES. PROCEEDINGS. IEEE SAN FRANCISCO, CA USA 29 MARCH-2 APRIL 1998, p. 272-281. |                |
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